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Application No. 10 1020, 208	Prepared by	HN	Tracking Number	05894361
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a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449	
b. Applicant(s)	g. Disclaimer	I. Print Fig.	q. PTOL-85b	
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract	
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs	
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other	

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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR I PATENT IN REEXAMINATION		ATTORNEY DOCKET NO.
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			ART UNIT	PAPER
				031504

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Attached is the PTO-1449 that initial to consider the cited references that missing from previous mailed.

Gene N Auduong Primary Examiner Art Unit: 2818

Electronic Information Disclosure Statement

System and Method to Screen Defect Related Reliability Failures in CMOS SRMAS

Application:

Confirmation:

7966

Applicant(s):

Surya Bhattacharya

Docket Number:

1875.0330001

Group Art Unit:

2818

Examiner:

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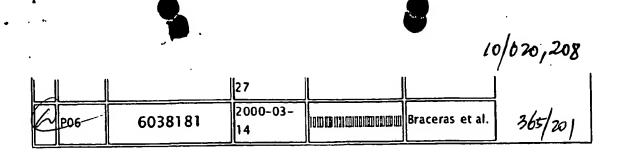
US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class Subclass
12	P01	5745415	1998-04- 28	ووه روس من	Marr	365 201
0	PO2	5661729	1997-08- 26	101 611 COLOR COLOR 100 COLOR 101 CO	Miyazaki et al.	714/719
	P03	6154403	2000-11- 28	m colo n co co co co co	Tanzawa et al.	365/203
Z	P04 -	6115304	2000-09- 05	ين أما ها ها ها إمال ها المالي والوا	Suga	365/201
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